First Named Inventor Chistophe Chevallier Unknown 10/784,37 Z INFORMATION DISCLOSURE Serial No. **STATEMENT** Filed Herewith Filing Date FORM PTO-1449 Group Art Unit Unknown 2827 **Examiner Name** Unknown V. Nosculla Confirmation No. Unknown-Attorney Docket No. 400.069US08

Title: FLASH ARRAY IMPLEMENTATION WITH LOCAL AND GLOBAL BIT LINES
Sheet 1 of 1

U.S. Patent References				
Examiner Initials	Document/Patent No.	Issue/Publication Date	Name	Filing Date
\mathcal{W}	5,777,922	July 7, 1998	Choi	October 18, 1996
W	5,825,782	10-20-1998	Roohparvar	01-22-1996
W	5,894,437	April 13, 1999	Chang	January 23, 1998
W,	5,898,637	04-27-1999	Lakhani et al.	01-06-1997
w,	5,996,106	11-30-1999	Seyyedy	02-04-1997
W	6,304,504	10-16-2001	Chevallier et al.	08-30-2000
		<u> </u>		
- · · · · · · · · · · · · · · · · · · ·				
		<u> </u>		

Foreign Patent References						
Examiner Initials	aminer Initials Foreign Patent		Name	Publication Date	177	
/	Country	No.				
W	wo	96/31882	Micron Technology, Inc.	10/10/96		

Other References			
Examiner Initials	Author, Title, Date, Pages, etc.		

·	

Examiner	1/12-12101	Date	2/24/0	
Signature	V. Nowen	Considered	2/24/03	
*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not				

*Examiner: Initial if citation considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if no in conformance and not considered. Include copy of this form with next communication to applicant.